Response to Reviewer 2 Comments

**Point 1:** Very good paper on testing method of STATCOM converters, however some unclear parts were found in the manuscript

**Response 1:** Thank you very much for your recognition of this paper. In the revised version, we have corrected the mentioned errors and other similar mistakes.

**Point 2:** In Fig. 3 iC is the longest among current vectors however the proposed testing method is based on assumption that this current has to be close to zero.

**Response 2:** In this proposed testing method, $i_c$ is actually very small. We redraw Figure 3 in the revised version and corrected the drawing error.

**Point 3:** In Fig. 4 two names of "uC" are shown but only one vector "uC" can be presented

**Response 3:** In the revised version, we redraw Figure 4 and removed the extra labels.

**Point 4:** In Fig. 5 modulator blocks are referred as CSPWM however in the text CPSPWM name is used

**Response 4:** In the revised Figure 5, we corrected this error.

**Point 5:** The major part of the paper is devoted to the start-up procedure of the converter, in three figures dc voltages are shown, however the capacitance of dc-link capacitors are not given in the table 1. Number of submodules are also not presented however the time durations shown in start-up waveforms are closely related to the capacitance and number of of submodules. I recommend to comment the lack of this parameters or include them.

**Response 5:** In the revised version, both the number of submodules and the capacitance are given. In addition, the parameters of controllers are also given in Table 1. (In the simulation, the number of submodules in each phase is set as four to speed up simulation. Although it is much less than the actual number of STATCOM, it can also represent the MMC structure and then it is sufficient to verify the feasibility of the proposed method for MMC-STATCOM.)